



520.43306X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Osamu SUZUKI, et al.  
Serial No: 10/722,597  
Filed: November 28, 2003  
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
SEMICONDUCTOR INTEGRATED CIRCUIT CHIP THEROF  
Group: 2822  
Examiner: Monica LEWIS

**AMENDMENT**

**Mail Stop: Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 2, 2008

Sir:

This Amendment is being filed in response to the outstanding non-Final Office Action, dated April 2, 2008, the period for response for which Extension of Time is requested in the attached Petition for Extension of Time; please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Claims;** and

**Remarks** are included following the amendments.